

74LVC2T45 2-Bit Dual-Supply Bus Transceiver with Configurable Voltage Translation

GENERAL DESCRIPTION

The 74LVC2T45 is a 2-bit, dual-supply bus transceiver with configurable voltage translation. The device has two separate configurable power-supply rails. The A and B ports track the $V_{\rm CCA}$ supply and $V_{\rm CCB}$ supply respectively. The supply voltage pins accept any voltage from 1.65V to 5.5V. This makes the device suitable for low voltage bidirectional translation voltage nodes of 1.8V, 2.5V, 3.3V, and 5V.

The 74LVC2T45 features that allows two data buses asynchronously communicated. Either the A port outputs or the B port outputs can be activated by DIR logic levels. The DIR input circuit is supplied by V_{CCA} . When B port outputs are activated, the device allows the data to transmit from A bus to B bus. On the contrary, when A port outputs are activated, the device allows the data to transmit from B bus to A bus. The input circuit is always active on the two ports. A logic high or low must be set to avoid excessive supply current.

FEATURES

- V_{CCA} Supply Voltage Range: 1.65V to 5.5V
- V_{CCB} Supply Voltage Range: 1.65V to 5.5V
- DIR Input Circuit Referenced to V_{CCA}
- +32mA/-32mA Output Current
- Data Rates
 - 420Mbps (3.3V to 5V Translation)
 - 210Mbps (Translate to 3.3V)
 - 140Mbps (Translate to 2.5V)
 - 75Mbps (Translate to 1.8V)
- Outputs in High-Impedance State when V_{CCA} or V_{CCB} = 0V
- -40°C to +125°C Operating Temperature Range
- Available in Green MSOP-8 and XTDFN-1.35×1-8L Packages

APPLICATIONS

Personal Electronic Devices
Industrial and Enterprise Devices
Telecommunications



PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
74LVC2T45	MSOP-8	-40°C to +125°C	74LVC2T45XMS8G/TR	GJX XMS8 XXXXX	Tape and Reel, 4000
742402140	XTDFN-1.35×1-8L	-40°C to +125°C	74LVC2T45XXET8G/TR	4PX	Tape and Reel, 5000

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code. X = Date Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS (1)

	•
Supply Voltage Range, V _{CCA}	0.5V to 6.5V
Supply Voltage Range, V _{CCB}	0.5V to 6.5V
Input Voltage Range, V _I (2)	0.5V to 6.5V
Output Voltage Range, V _O ⁽²⁾	
High-Impedance State	0.5V to 6.5V
High-State or Low-State	
A Ports0.5V to MIN (6.5	$5V, V_{CCA} + 0.5V)$
B Ports0.5V to MIN (6.5	$5V, V_{CCB} + 0.5V)$
Input Clamp Current, I _{IK} (V _I < 0)	50mA
Output Clamp Current, I _{OK} (V _O < 0)	50mA
Output Current, I _O	
High-State or Low-State	±50mA
Supply Current, I _{CCA} or I _{CCB}	100mA
Ground Current, I _{GND}	100mA
Junction Temperature (3)	+150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility	
HBM	4000V
CDM	1000V

OVERSTRESS CAUTION

1. Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the

device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

- 2. The input negative-voltage and output voltage ratings may be exceeded if the input and output current ratings are observed.
- 3. The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.

ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

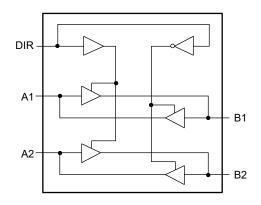
SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

74LVC2T45

2-Bit Dual-Supply Bus Transceiver with Configurable Voltage Translation

RECOMMENDED OPERATING CONDITIONS	Low-Level Output Current, IoL
Supply Voltage Range, V _{CCA} 1.65V to 5.5V	Input Transition Rise or Fall Rate, Δt/ΔV
Supply Voltage Range, V _{CCB} 1.65V to 5.5V	Data Inputs
Input Voltage Range, V _I 0V to 5.5V	V _{CCI} = 1.65V to 1.95V 20ns/V (MAX)
Output Voltage Range, Vo	V _{CCI} = 2.3V to 2.7V 20ns/V (MAX)
High-Impedance State0V to 5.5V	V _{CCI} = 3V to 3.6V 10ns/V (MAX)
High-State or Low-State	V _{CCI} = 4.5V to 5.5V 5ns/V (MAX)
A Ports0V to V _{CCA}	Control Input
B Ports0V to V _{CCB}	V _{CCI} = 1.65V to 5.5V 5ns/V (MAX)
High-Level Output Current, Ioh32mA	Operating Temperature Range40°C to +125°C

LOGIC DIAGRAM



FUNCTION TABLE

SUPPLY VOLTAGE	CONTROL INPUT	INPUT/O	UTPUT ⁽¹⁾
V _{CCA} , V _{CCB}	DIR (2)	An	Bn
1.65V to 5.5V	L	An = Bn	Input
1.65V to 5.5V	Н	Input	Bn = An
GND (3)	X	Z	Z

H = High Voltage Level

L = Low Voltage Level

X = Don't Care

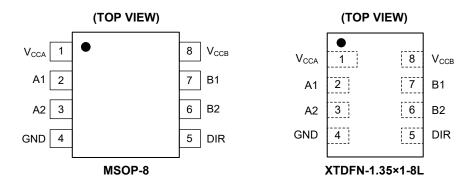
Z = High-Impedance State

NOTES:

- 1. The input circuit of the data I/O is always active.
- 2. The DIR input circuit is referenced to $\ensuremath{V_{\text{CCA.}}}$
- 3. If at least one of V_{CCA} or V_{CCB} is at GND level, the outputs in High-Impedance State.



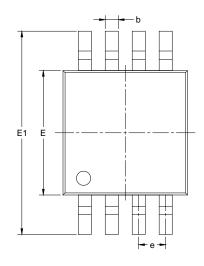
PIN CONFIGURATIONS

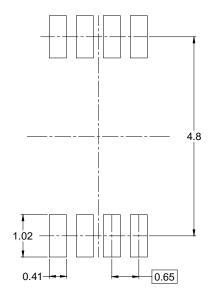


PIN DESCRIPTION

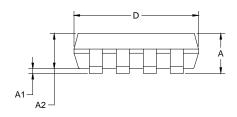
Р	IN	NAME	FUNCTION	
MSOP-8	XTDFN-1.35×1-8L	INAIVIE	FONCTION	
1	1	V _{CCA}	Supply Voltage on A Ports.	
2	2	A1	Input/Output. It tracks the V _{CCA} supply.	
3	3	A2	Input/Output. It tracks the V _{CCA} supply.	
4	4	GND	Ground.	
5	5	DIR	Direction Control Signal.	
6	6	B2	Input/Output. It tracks the V _{CCB} supply.	
7	7	B1	Input/Output. It tracks the V _{CCB} supply.	
8	8	V _{CCB}	Supply Voltage on B Ports.	

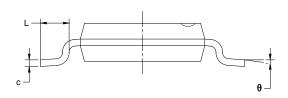
PACKAGE OUTLINE DIMENSIONS MSOP-8





RECOMMENDED LAND PATTERN (Unit: mm)

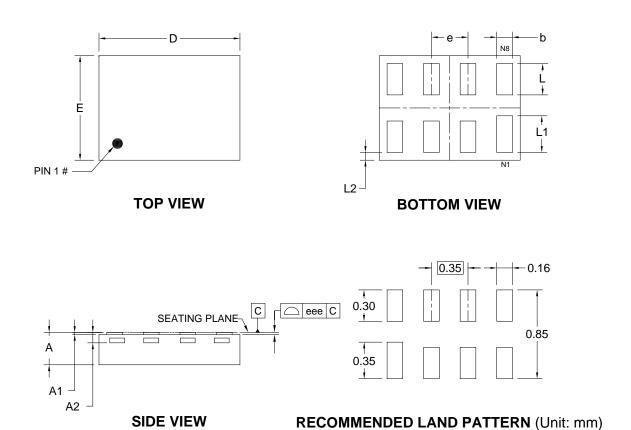




Symbol	-	nsions meters	Dimensions In Inches		
	MIN	MAX	MIN	MAX	
Α	0.820	1.100	0.032	0.043	
A1	0.020	0.150	0.001	0.006	
A2	0.750	0.950	0.030	0.037	
b	0.250	0.380	0.010	0.015	
С	0.090	0.230	0.004	0.009	
D	2.900	3.100	0.114	0.122	
E	2.900	3.100	0.114	0.122	
E1	4.750	5.050	0.187	0.199	
е	e 0.650 BSC		0.026	BSC	
L	0.400	0.800	0.016	0.031	
θ	0°	6°	0°	6°	

- Body dimensions do not include mode flash or protrusion.
 This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS XTDFN-1.35×1-8L



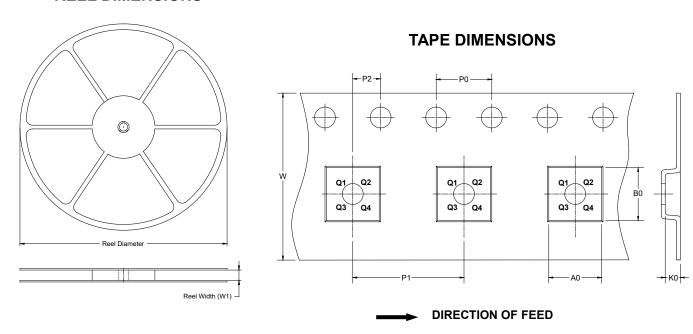
Symbol	Dimensions In Millimeters					
Symbol	MIN	MOD	MAX			
Α	-	0.310	0.330			
A1	0.000	-	0.050			
A2		0.100 REF				
D	1.250 1.350 1.450					
E	0.900	1.000	1.100			
b	0.110 0.160		0.210			
е	0.350 BSC					
L	0.250	0.300	0.350			
L1	0.300	0.350	0.400			
L2	0.075 REF					
eee	- 0.050 -					

NOTE: This drawing is subject to change without notice.



TAPE AND REEL INFORMATION

REEL DIMENSIONS

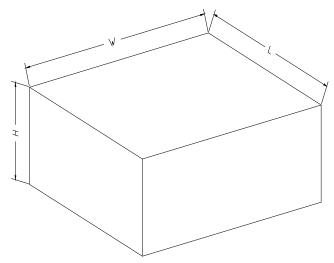


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
MSOP-8	13"	12.4	5.20	3.30	1.50	4.0	8.0	2.0	12.0	Q1
XTDFN-1.35×1-8L	7"	9.5	1.21	1.51	0.39	4.0	4.0	2.0	8.0	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7" (Option)	368	227	224	8
7"	442	410	224	18
13"	386	280	370	5