

### GENERAL DESCRIPTION

The 74AHC573 device is an octal transparent D-type latch with 3-state outputs. The supply voltage can accept any voltage from 2.0V to 5.5V.

The output enable  $\overline{OE}$  input is active low. In this case, when latch enable LE input is taken high, the Q outputs follow the data D inputs. When LE input is taken low, the Q outputs are latched to retain the set-up data. When  $\overline{OE}$  is high, all outputs are in high-impedance state.

The output enable  $\overline{OE}$  input can make all outputs in high/low logic levels or high-impedance state, which has no influence on the inner working of the latches. When the outputs are in a high-impedance state, the latch can retain old data or enter new data.

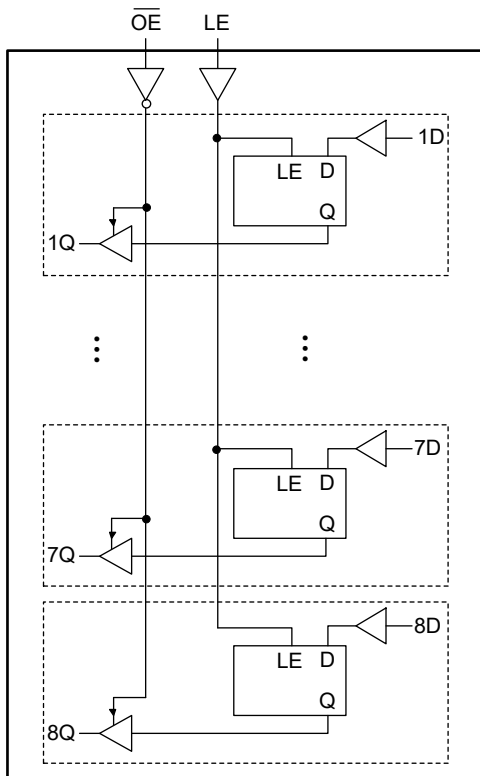
### FEATURES

- **Wide Supply Voltage Range: 2.0V to 5.5V**
- **Input Accept Voltage Higher than the Supply Voltage**
- **+8mA/-8mA Output Current**
- **3-State Outputs Drive Bus Lines Directly**
- **-40°C to +125°C Operating Temperature Range**
- **Available in Green SOIC-20 and TSSOP-20 Packages**

### APPLICATIONS

Computing: Server, PC and Notebook  
 Network Switch  
 Telecom Infrastructure

### LOGIC DIAGRAM



### FUNCTION TABLE

INPUT			OUTPUT
$\overline{OE}$	LE	nD	nQ
L	H	H	H
L	H	L	L
L	L	X	Q <sub>0</sub>
H	X	X	Z

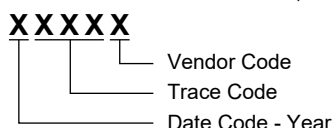
H = High Voltage Level  
 L = Low Voltage Level  
 Z = High-Impedance State  
 X = Don't Care

### PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
74AHC573	SOIC-20	-40°C to +125°C	74AHC573XS20G/TR	74AHC573XS20 XXXXX	Tape and Reel, 1500
	TSSOP-20	-40°C to +125°C	74AHC573XTS20G/TR	08PXTS20 XXXXX	Tape and Reel, 4000

### MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

### ABSOLUTE MAXIMUM RATINGS <sup>(1)</sup>

Supply Voltage, $V_{CC}$ .....	-0.5V to 7.0V
Input Voltage, $V_I$ <sup>(2)</sup> .....	-0.5V to 7.0V
Output Voltage, $V_O$ <sup>(2)</sup> .....	-0.5V to MIN (7.0V, $V_{CC} + 0.5V$ )
Input Clamping Current, $I_{IK}$ ( $V_I < 0V$ ).....	-20mA
Output Clamping Current, $I_{OK}$ ( $V_O < 0V$ or $V_O > V_{CC}$ )...	$\pm 20mA$
Continuous Output Current, $I_O$ ( $V_O = 0V$ to $V_{CC}$ ).....	$\pm 25mA$
Continuous Current ( $V_{CC}$ or GND).....	$\pm 75mA$
Junction Temperature <sup>(3)</sup> .....	+150°C
Storage Temperature Range.....	-65°C to +150°C
Lead Temperature (Soldering, 10s).....	+260°C
ESD Susceptibility	
HBM.....	4000V
CDM.....	1000V

### RECOMMENDED OPERATING CONDITIONS

Supply Voltage, $V_{CC}$ .....	2.0V to 5.5V
Input Voltage, $V_I$ .....	0V to 5.5V
Output Voltage, $V_O$ .....	0V to $V_{CC}$
Output Current, $I_O$ .....	$\pm 8mA$
Input Transition Rise or Fall Rate, $\Delta t/\Delta V$ .....	100ns/V (MAX)
Operating Temperature Range.....	-40°C to +125°C

### OVERSTRESS CAUTION

- Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.
- The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.
- The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.

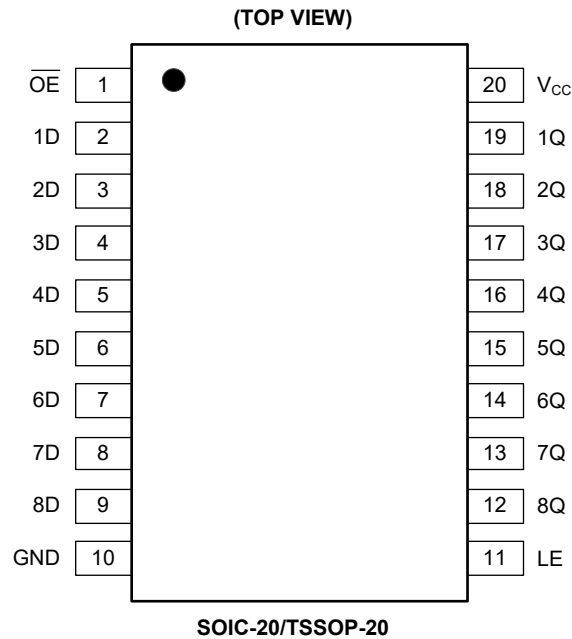
### ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

### DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

## PIN CONFIGURATION

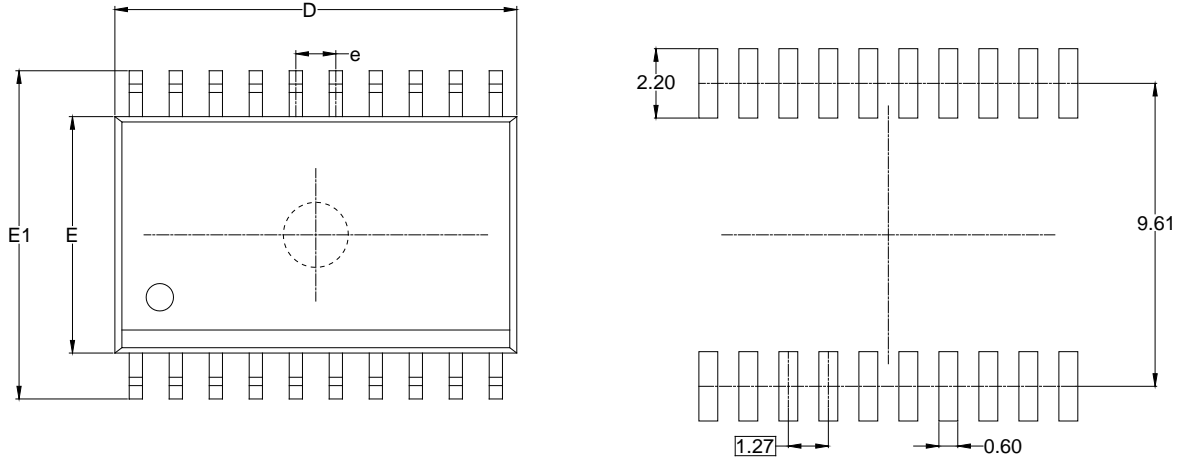


## PIN DESCRIPTION

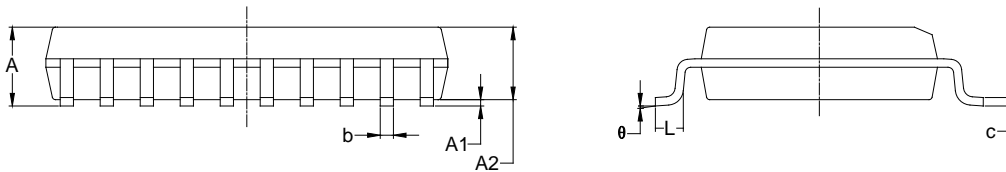
PIN	NAME	FUNCTION
1	$\overline{OE}$	Output Enable Input (Active Low).
2, 3, 4, 5, 6, 7, 8, 9	1D, 2D, 3D, 4D, 5D, 6D, 7D, 8D	Data Inputs.
19, 18, 17, 16, 15, 14, 13, 12	1Q, 2Q, 3Q, 4Q, 5Q, 6Q, 7Q, 8Q	Data Outputs.
11	LE	Latch Enable Input (Active High).
10	GND	Ground.
20	V <sub>CC</sub>	Supply Voltage.

PACKAGE OUTLINE DIMENSIONS

SOIC-20



RECOMMENDED LAND PATTERN (Unit: mm)

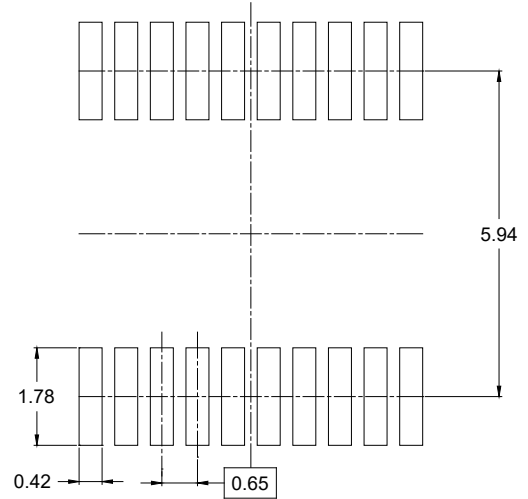
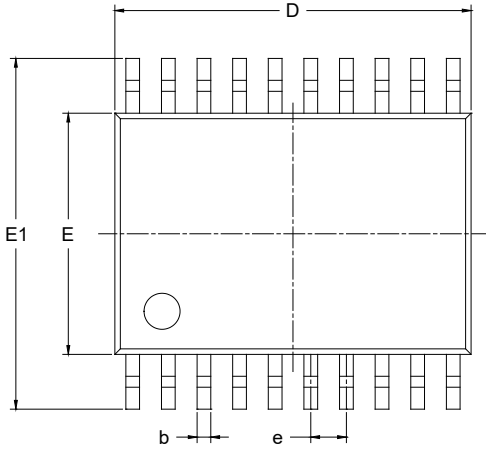


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	MAX	MIN	MAX
A	2.350	2.650	0.093	0.104
A1	0.100	0.300	0.004	0.012
A2	2.100	2.500	0.083	0.098
b	0.330	0.510	0.013	0.020
c	0.204	0.330	0.008	0.013
D	12.520	13.000	0.493	0.512
E	7.400	7.600	0.291	0.299
E1	10.210	10.610	0.402	0.418
e	1.27 BSC		0.050 BSC	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

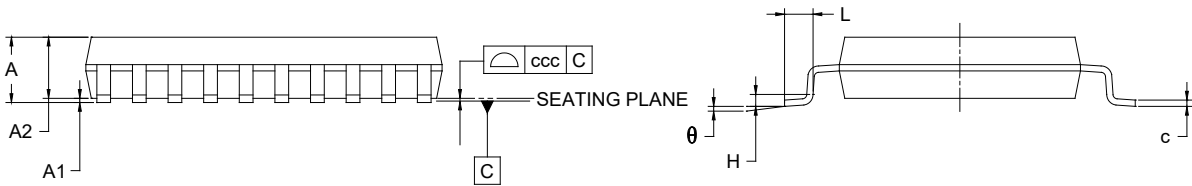
- NOTES:  
 1. Body dimensions do not include mold flash or protrusion.  
 2. This drawing is subject to change without notice.

PACKAGE OUTLINE DIMENSIONS

TSSOP-20



RECOMMENDED LAND PATTERN (Unit: mm)



Symbol	Dimensions In Millimeters		
	MIN	MOD	MAX
A	-	-	1.200
A1	0.050	-	0.150
A2	0.800	-	1.050
b	0.190	-	0.300
c	0.090	-	0.200
D	6.400	-	6.600
E	4.300	-	4.500
E1	6.200	-	6.600
e	0.650 BSC		
L	0.450	-	0.750
H	0.250 TYP		
θ	0°	-	8°
ccc	0.100		

NOTES:

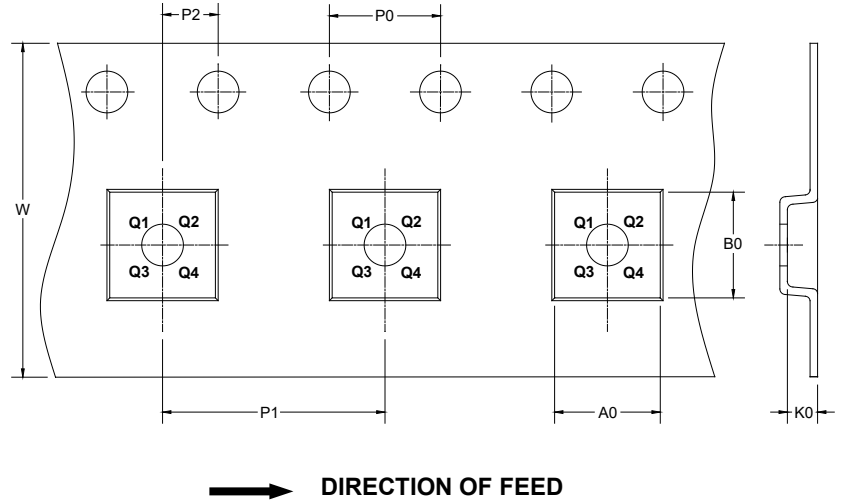
1. Body dimensions do not include mode flash or protrusion.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-153.

## TAPE AND REEL INFORMATION

### REEL DIMENSIONS



### TAPE DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

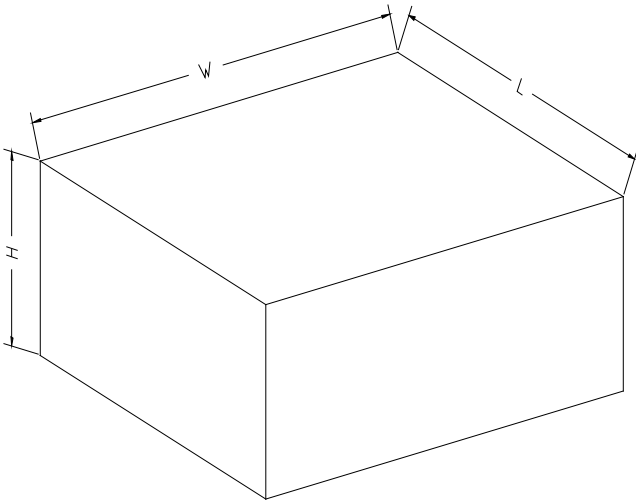
### KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-20	13"	24.4	10.90	13.30	3.00	4.0	12.0	2.0	24.0	Q1
TSSOP-20	13"	16.4	6.80	6.90	1.50	4.0	8.0	2.0	16.0	Q1

000001

# PACKAGE INFORMATION

## CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

## KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13"	386	280	370	5

DD0002