SGM8437-1
Current Feedback, Wide-Band High-Current Output Amplifier

GENERAL DESCRIPTION

The SGM8437-1 is a current feedback, wide-band high-current output amplifier with high voltage, low noise and high slew rate performance. These features make SGM8437-1 very suitable for wide-band heavy load applications.

The SGM8437-1 can operate from 8V to 30V single supply or from ±4V to ±15V dual supplies. And it maintains wide bandwidth and high linearity over the whole full-scale range of power supply.

A disable control (DIS) pin is used to control the operation modes of the device. When DIS pin is high or floating, SGM8437-1 is in power-down mode. When DIS pin is low, SGM8437-1 is in full-power working mode.

The SGM8437-1 is available in a Green SOIC-8 (Exposed Pad) package. It operates over an ambient temperature range of -40℃ to +85℃.

FEATURES

- **Current Feedback Amplifier**
- **Support Single or Dual Power Supplies: 8V to 30V or ±4V to ±15V**
- **Supply Current: 9.5mA (TYP)**
- **Power-Down Current: 35μA (TYP)**
- **Low Input Voltage Noise Density: 10nV/$\sqrt{Hz}$**
- **High Slew Rate for Differential Signal: 800V/μs**
- **Stable at Gain ≥ 2**
- **Output Over-Voltage Protection and Voltage Clamping Protection**
- **Over-Temperature Protection**
- **Disable Control Pins for Low-Power Design**
- **-40℃ to +85℃ Operating Temperature Range**
- **Available in a Green SOIC-8 (Exposed Pad) Package**

APPLICATIONS

Test Equipment Amplifiers
Cable Drivers

TYPICAL APPLICATION

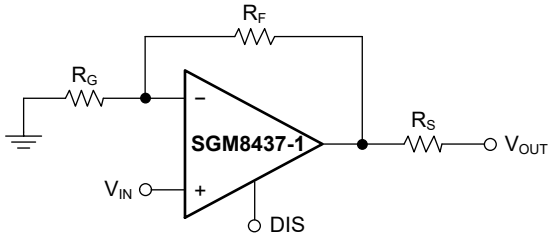


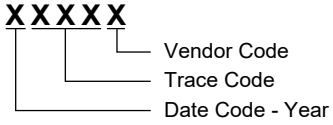
Figure 1. Typical Application Circuit

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM8437-1	SOIC-8 (Exposed Pad)	-40℃ to +85℃	SGM8437-1YPS8G/TR	SGM 84371YPS8 XXXXX	Tape and Reel, 4000
			SGM8437-1YPS8SG/TR	SGM 84371YPS8 XXXXX	Tape and Reel, 500

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, $+V_S$ to $-V_S$ ..... -0.3V to 32V
$+V_S$ Voltage to GND ..... -0.3V to 30V
$-V_S$ Voltage to GND ..... -30V to 0.3V
DIS Voltage to GND ..... -0.3V to 5.5V
Package Thermal Resistance
SOIC-8 (Exposed Pad), θ_{JA} ..... 39.3℃/W
Junction Temperature ..... +150℃
Storage Temperature Range ..... -65℃ to +150℃
Lead Temperature (Soldering, 10s) ..... +260℃
ESD Susceptibility (1) (2)
HBM ..... ±4000V
CDM ..... ±1000V

NOTES:

1. For human body model (HBM), all pins comply with ANSI/ESDA/JEDEC JS-001 specifications.

2. For charged device model (CDM), all pins comply with ANSI/ESDA/JEDEC JS-002 specifications.

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range ..... -40℃ to +85℃

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

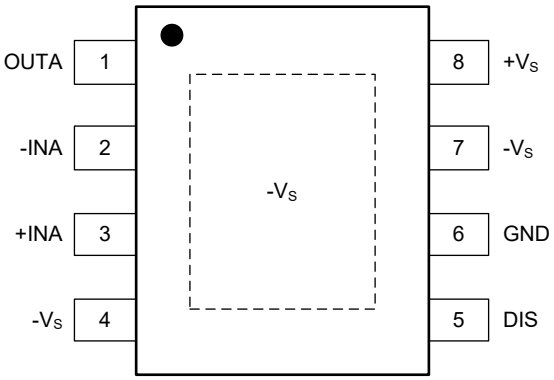
This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION

(TOP VIEW)



SOIC-8 (Exposed Pad)

PIN DESCRIPTION

PIN	NAME	FUNCTION
1	OUTA	Output of Amplifier A.
2	-INA	Inverting Input of Amplifier A.
3	+INA	Non-Inverting Input of Amplifier A.
4, 7	$-V_S$	Negative Power Supply Voltage. For single power supply application, $-V_S$ pin must be connected to external ground. For dual power supplies application, $-V_S$ pin must be connected to external -4V to -15V negative power supply.
5	DIS	Disable Control Pin. The SGM8437-1 is in power-down (disabled) mode if the DIS pin is floating.
6	GND	Ground. The GND pin must be connected with external ground.
8	$+V_S$	Positive Power Supply for Amplifier. (8V to 30V for single power supply and +4V to +15V for dual power supplies.)
Exposed Pad	$-V_S$	Must be connected to $-V_S$ for optimal thermal performance. Connecting to other pins is not allowed.

ELECTRICAL CHARACTERISTICS

(V_S = 8V to 30V, V_{CM} = 1/2V_S, R_F = 1.2kΩ, R_L = 50Ω terminated to 1/2V_S, A_V = 10, Full = -40℃ to +85℃, typical values are at T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Supply Characteristics							
Operating Voltage Range	V_S		Full	8		30	V
Positive Supply Current	$+I_S$ (Full-Power)	All outputs at 0V	+25℃		9.5	12	mA
			Full			13	
Negative Supply Current	$-I_S$ (Full-Power)	All outputs at 0V	+25℃	-12	-9.5		mA
			Full	-13			
Positive Supply Current	$+I_S$ (Power-Down)	All outputs at 0V, V_{DIS} = 3.3V, V_S = 28V	+25℃		35	45	μA
			Full			55	
Negative Supply Current	$-I_S$ (Power-Down)	All outputs at 0V, V_{DIS} = 3.3V, V_S = 28V	+25℃	-35	-26		μA
			Full	-45			
Power Supply Rejection Ratio	PSRR+	V_S = 8V to 30V, V_{CM} = 4V	+25℃	89	96		dB
			Full	80			
	PSRR-	V_S = 8V to 30V, V_{CM} = 4V to 26V	+25℃	88	96		
			Full	80			
Input Characteristics							
Input Offset Voltage	V_{OS}	I_{OUT} = 0mA	+25℃		6	13	mV
			Full			20	
Input Offset Voltage Drift	$\Delta V_{OS}/\Delta T$		Full		0.1		mV/℃
Inverting Input Bias Current	$-I_B$		+25℃		19	36	μA
			Full			47	
Inverting Input Bias Current Drift	$\Delta I_B/\Delta T$		Full		0.2		μA /℃
Non-Inverting Input Bias Current	$+I_B$		+25℃		12	45	nA
			Full			300	
Non-Inverting Input Bias Current Drift	$\Delta I_{B+}/\Delta T$		Full		0.2		nA/℃
Input Common Mode Voltage Range	V_{CM}		Full	(-V_S) + 4		(+V_S) - 4	V
Common Mode Rejection Ratio	CMRR	V_S = 12V, V_{CM} = 4V to 8V	+25℃	75	82		dB
			Full	65			
		V_S = 30V, V_{CM} = 4V to 26V	+25℃	84	90		
			Full	75			
Transimpedance (1)	R_{OL}		Full		18		MΩ
Input High Voltage	V_{IH}	DIS input	Full	2			V
Input Low Voltage	V_{IL}	DIS input	Full			0.8	V
Input Pin Current	I_{IH}	DIS input, V_{DIS} = 3.3V	Full		-0.7	4	μA
	I_{IL}	DIS input, V_{DIS} = 0V	Full	-4	-2		
Output Characteristics							
Output Voltage Swing from Either Supply Rail	V_{OUT}	V_S = 30V, R_L = 50Ω	+25℃		2.5	3	V
		V_S = 30V, R_L = 100Ω	+25℃		2		
		V_S = 30V, R_L = open	+25℃		1.5		
Output Current	I_{OUT}	V_S = 30V, R_L = 20Ω	+25℃		±0.5		A

NOTE: 1. Specified by design.

ELECTRICAL CHARACTERISTICS (continued)

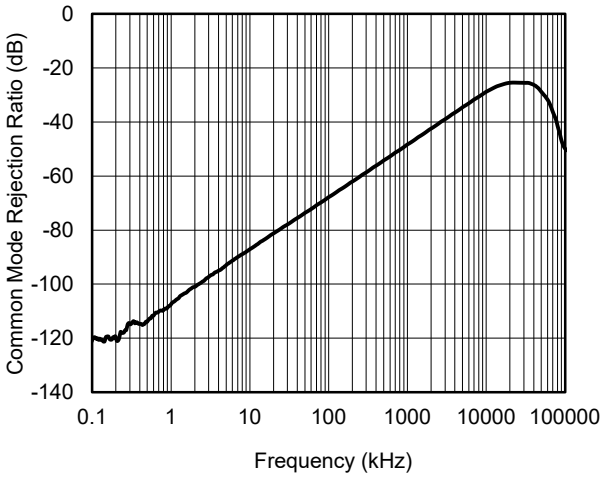
(V_S = 8V to 30V, V_{CM} = 1/2V_S, R_F = 1.2kΩ, R_L = 50Ω terminated to 1/2V_S, A_V = 10, Full = -40℃ to +85℃, typical values are at T_A = +25℃, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	TEMP	MIN	TYP	MAX	UNITS
Dynamic Performance							
-3dB Small-Signal Bandwidth	BW	V_S = 30V, V_{OUT} = 2V_{P-P}	+25℃		75		MHz
-3dB Large-Signal Bandwidth	BW	V_S = 30V, V_{OUT} = 10V_{P-P}	+25℃		35		MHz
2nd Harmonic Distortion	HD2	V_S = 30V, f_C = 1MHz, V_{OUT} = 10V_{P-P}	+25℃		-68		dBc
		V_S = 30V, f_C = 2MHz, V_{OUT} = 10V_{P-P}	+25℃		-65		
		V_S = 30V, f_C = 3MHz, V_{OUT} = 10V_{P-P}	+25℃		-59		
		V_S = 30V, f_C = 10MHz, V_{OUT} = 10V_{P-P}	+25℃		-39		
3rd Harmonic Distortion	HD3	V_S = 30V, f_C = 1MHz, V_{OUT} = 10V_{P-P}	+25℃		-72		dBc
		V_S = 30V, f_C = 2MHz, V_{OUT} = 10V_{P-P}	+25℃		-65		
		V_S = 30V, f_C = 3MHz, V_{OUT} = 10V_{P-P}	+25℃		-61		
		V_S = 30V, f_C = 10MHz, V_{OUT} = 10V_{P-P}	+25℃		-52		
Slew Rate (Differential Signal)	SR	V_S = 30V, V_{OUT} = 20V_{P-P}	+25℃		800		V/μs
Turn-On/Off Time	t_{EN}	From disable to enable time	+25℃		10		μs
	t_{DIS}	From enable to disable time	+25℃		160		ns
Noise							
Input Voltage Noise Density	e_n	f = 1MHz	+25℃		10		nV/$\sqrt{Hz}$
Over-Temperature Protection							
Over-Temperature Protection					150		℃
Over-Temperature Protection Hysteresis					5		℃

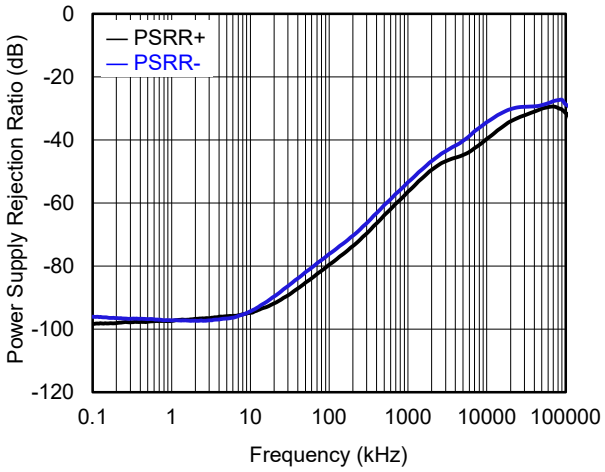
TYPICAL PERFORMANCE CHARACTERISTICS

At T_A = +25℃, V_S = ±15V, unless otherwise noted.

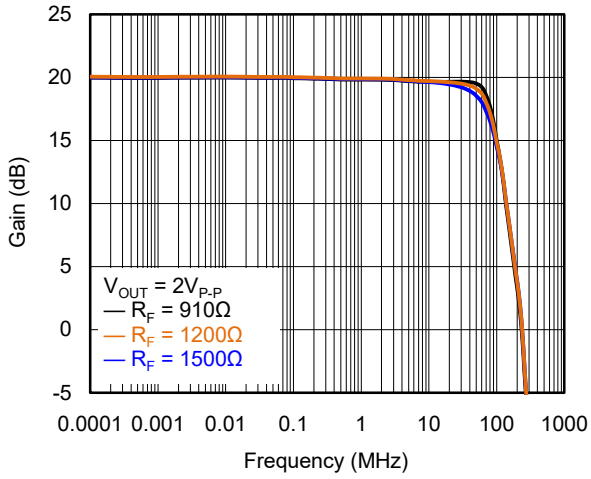
CMRR vs. Frequency



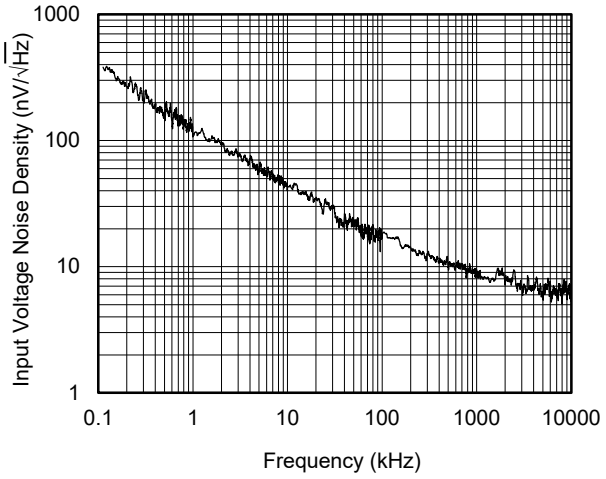
PSRR vs. Frequency



Gain vs. Frequency



Input Voltage Noise Density vs. Frequency



FUNCTIONAL BLOCK DIAGRAM

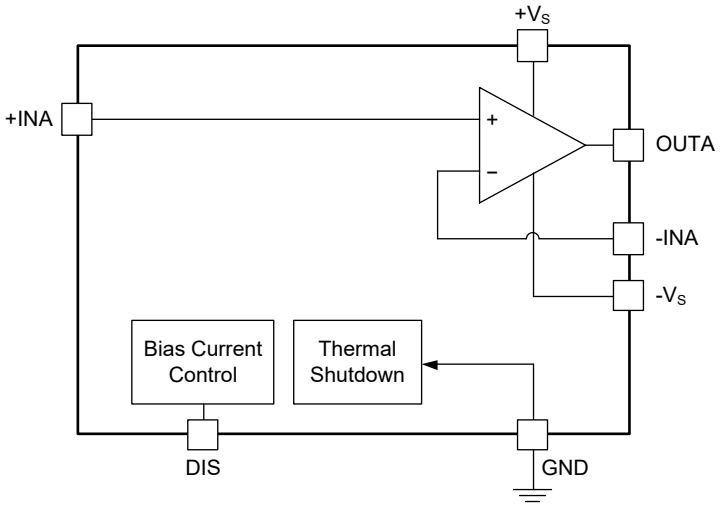


Figure 2. Block Diagram

APPLICATION INFORMATION

Figure 1 shows a typical application circuit for SGM8437-1.

Power Control Function

The SGM8437-1 supports power control operation. Its supply current is controlled by the digital inputs DIS. DIS pin is pulled high internally. The device immediately enters power-down mode when DIS pin is floating.

The truth table of the SGM8437-1 is shown in Table 1.

Table 1. Working Modes of SGM8437-1

DIS Pin	Operation
0	Full-Power Working Mode.
1	Power-Down Mode.
Floating	Power-Down Mode.

Breakdown Supply Voltage

If the amplifier is being used in an application that is part of a regulated power grid, the ability to withstand a supply voltage that is higher than the recommended voltage is important to ensure robustness.

In order to estimate the margin beyond the maximum supply voltage, several randomly selected samples are tested to show the robustness of SGM8437-1.

Figure 3 shows the configuration of this test. The SGM8437-1 is tested by manually increasing the supply voltage in 1V steps while simultaneously recording the supply current. This operation is performed from 28V until internal device is breakdown. Three samples are subjected to this test, and one of their results is shown in Figure 4.

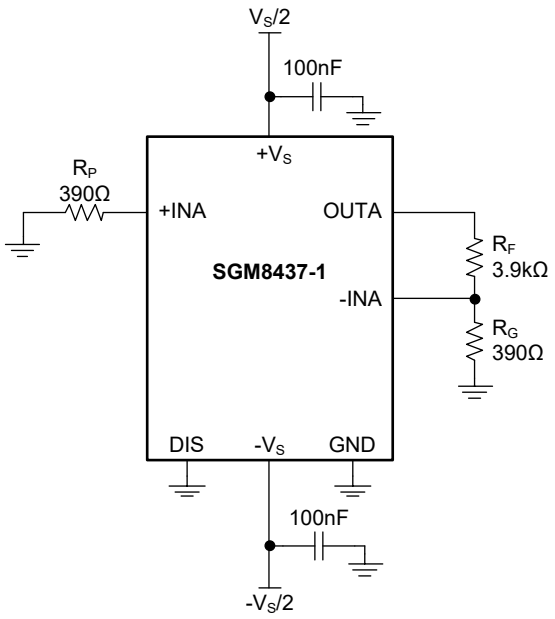


Figure 3. Breakdown Supply Voltage Test Configuration

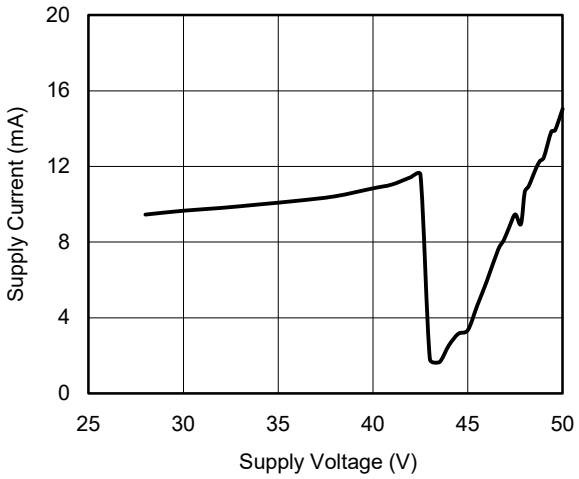


Figure 4. Supply Current vs. Supply Voltage

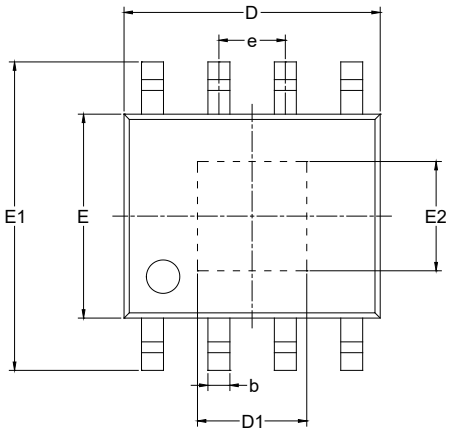
REVISION HISTORY

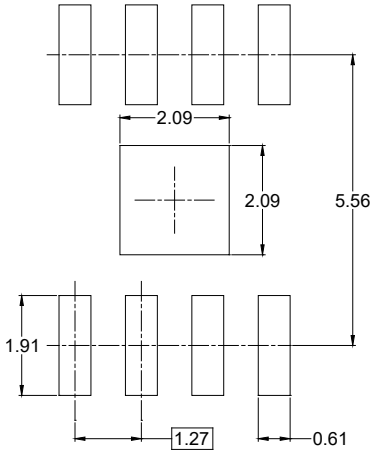
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

Changes from Original (MARCH 2025) to REV.A	Page
Changed from product preview to production data	All

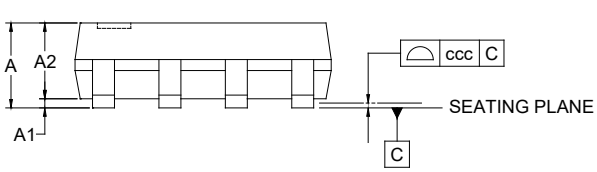
PACKAGE OUTLINE DIMENSIONS

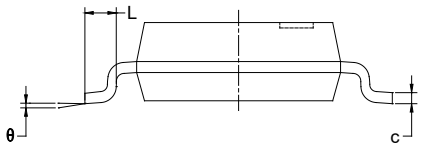
SOIC-8 (Exposed Pad)





RECOMMENDED LAND PATTERN (Unit: mm)



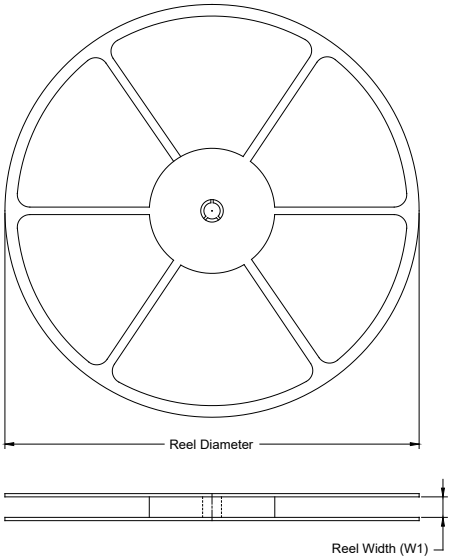


Symbol	Dimensions In Millimeters		
	MIN	NOM	MAX
A			1.700
A1	0.000	-	0.150
A2	1.250	-	1.650
b	0.330	-	0.510
c	0.170	-	0.250
D	4.700	-	5.100
D1	1.890	-	2.290
E	3.800	-	4.000
E1	5.800	-	6.200
E2	1.890	-	2.290
e	1.27 BSC		
L	0.400	-	1.270
θ	0°	-	8°
ccc	0.100		

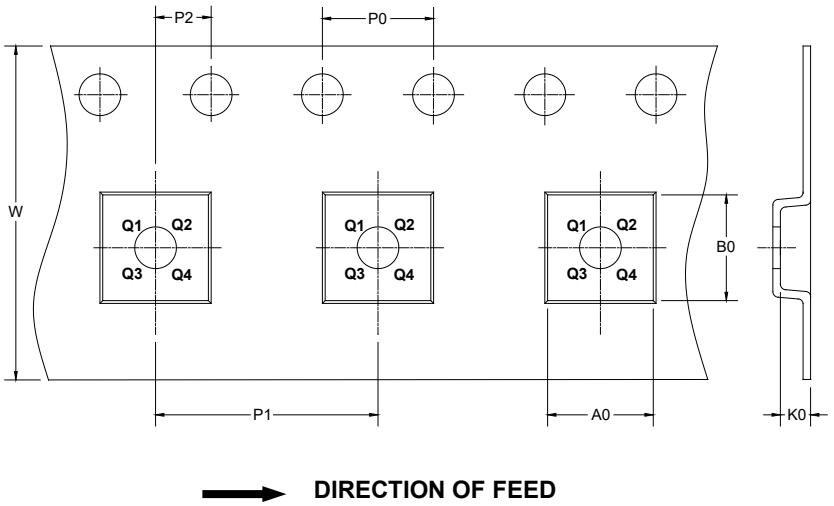
NOTES:
1. This drawing is subject to change without notice.
2. The dimensions do not include mold flashes, protrusions or gate burrs.
3. Reference JEDEC MS-012.

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



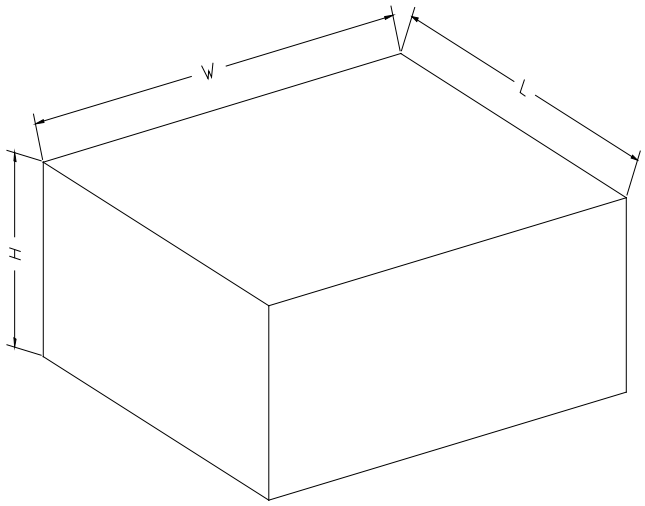
NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-8 (Exposed Pad)	13″	12.4	6.40	5.40	2.10	4.0	8.0	2.0	12.0	Q1

DD0001

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13″	386	280	370	5

DD0002