74AHC244Q
Octal Buffer/Line Driver with 3-State Outputs

GENERAL DESCRIPTION

The 74AHC244Q is an octal buffer/line driver with 3-state outputs. The device can be used as two 4-bit buffers or one 8-bit buffer. The $1\overline{OE}$ and $2\overline{OE}$ are two output enable inputs, and each controls four of the 3-state outputs. When $n\overline{OE}$ is set high, the outputs are in high-impedance state. When $n\overline{OE}$ is set low, data transmits from the nAn inputs to the nYn outputs.

The over-voltage tolerant inputs can come up to 5.5V. With this function, this device can be used as a translator in mixed voltage environment.

This device is AEC-Q100 qualified (Automotive Electronics Council Standard Q100 Grade 1) and the use of this device is suitable for automotive applications.

FEATURES

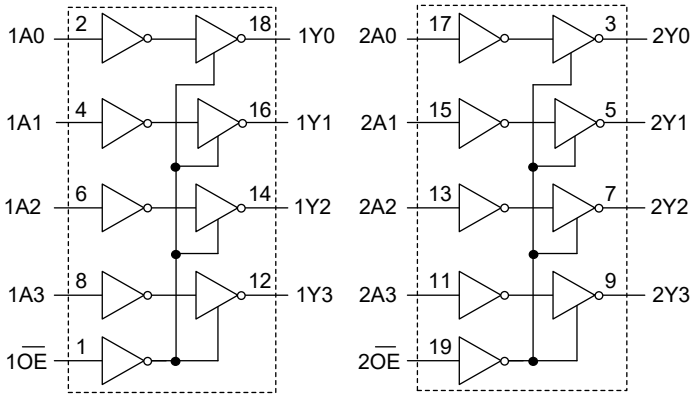
- **AEC-Q100 Qualified for Automotive Applications**
  Device Temperature Grade 1
  T_A = -40℃ to +125℃
- **Wide Supply Voltage Range: 2.0V to 5.5V**
- **All Inputs with Schmitt-Trigger Action**
- **Input Level: CMOS Level**
- **CMOS Low Power Dissipation**
- **Inputs are Over-Voltage Tolerant**
- **-40℃ to +125℃ Operating Temperature Range**
- **Available in a Green TSSOP-20 Package**

FUNCTION TABLE

CONTROL INPUT	INPUT	OUTPUT
$n\overline{OE}$	**nAn**	**nYn**
L	L	L
L	H	H
H	*X*	Z

H = High Voltage Level
L = Low Voltage Level
Z = High-Impedance State
X = Don't Care

LOGIC DIAGRAM

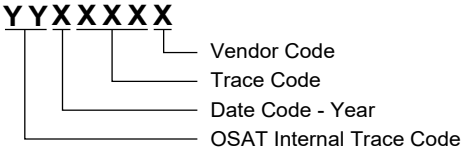


PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
74AHC244Q	TSSOP-20	-40℃ to +125℃	74AHC244QTS20G/TR	00ZTS20 YYXXXXX	Tape and Reel, 4000

MARKING INFORMATION

NOTE: YYXXXXX = Date Code, Trace Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS (1)

Supply Voltage, V_{CC} ..... -0.5V to 7.0V
Input Voltage, V_I (2) ..... -0.5V to 7.0V
Output Voltage, V_O (2) ..... -0.5V to MIN(7.0V, V_{CC} + 0.5V)
Input Clamp Current, I_{IK} (V_I < -0.5V) .....-20mA
Output Clamp Current, I_{OK} (V_O < -0.5V or V_O > V_{CC} + 0.5V)
..... ±20mA
Output Current, I_O (V_O = -0.5V to V_{CC} + 0.5V)..... ±25mA
Supply Current, I_{CC} 75mA
Ground Current, I_{GND}.....-75mA
Junction Temperature (3) .....+150℃
Storage Temperature Range .....-65℃ to +150℃
Lead Temperature (Soldering, 10s).....+260℃
ESD Susceptibility
HBM.....4000V
CDM 1000V

RECOMMENDED OPERATING CONDITIONS

Supply Voltage, V_{CC} .....2.0V to 5.5V
Input Voltage, V_I.....0V to 5.5V
Output Voltage, V_O..... 0V to V_{CC}
Output Current, I_O ..... ±8mA
Input Transition Rise and Fall Rate, Δt/ΔV
V_{CC} = 3.3V ± 0.3V ..... 100ns/V (MAX)
V_{CC} = 5.0V ± 0.5V ..... 20ns/V (MAX)
Operating Temperature Range .....-40℃ to +125℃

OVERSTRESS CAUTION

1. Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

2. The input and output negative voltage ratings may be exceeded if the input and output clamp current ratings are observed.

3. The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.

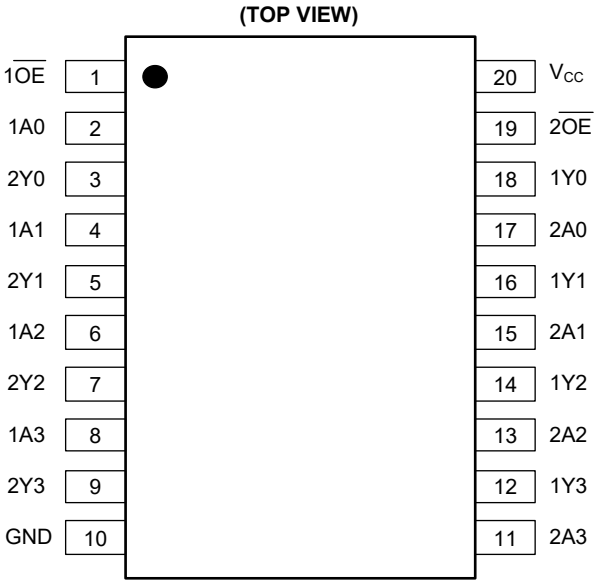
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION

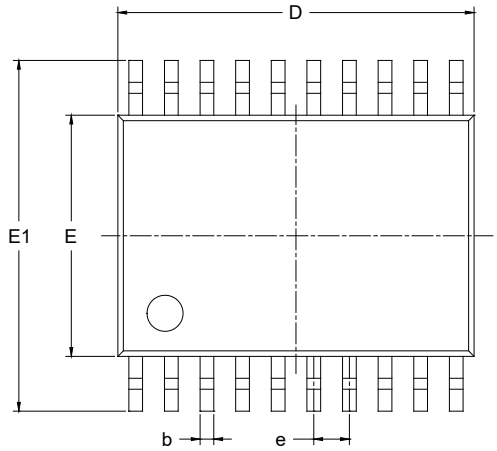


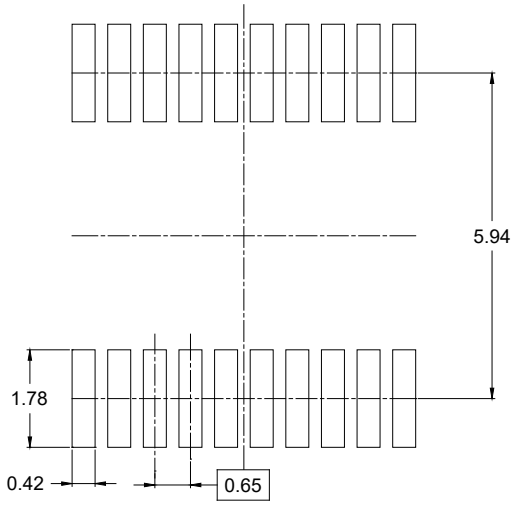
PIN DESCRIPTION

PIN	NAME	FUNCTION
1, 19	1$\overline{OE}$, 2$\overline{OE}$	Output Enable Inputs (Active Low).
2, 4, 6, 8	1A0, 1A1, 1A2, 1A3	Data Inputs.
18, 16, 14, 12	1Y0, 1Y1, 1Y2, 1Y3	Data Outputs.
10	GND	Ground.
17, 15, 13, 11	2A0, 2A1, 2A2, 2A3	Data Inputs.
3, 5, 7, 9	2Y0, 2Y1, 2Y2, 2Y3	Data Outputs.
20	V_{CC}	Supply Voltage.

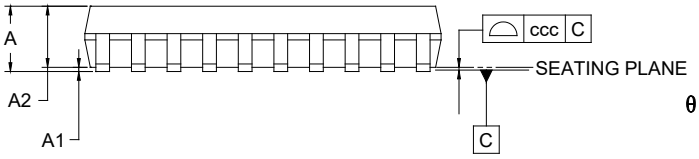
PACKAGE OUTLINE DIMENSIONS

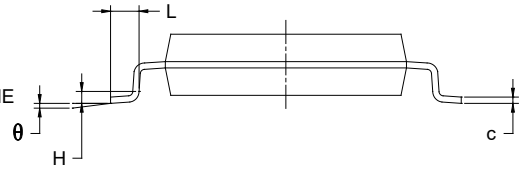
TSSOP-20





RECOMMENDED LAND PATTERN (Unit: mm)



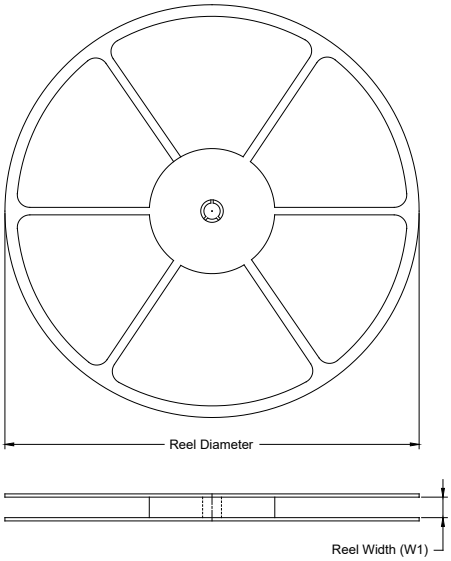


Symbol	Dimensions In Millimeters		
	MIN	MOD	MAX
A	-	-	1.200
A1	0.050	-	0.150
A2	0.800	-	1.050
b	0.190	-	0.300
c	0.090	-	0.200
D	6.400	-	6.600
E	4.300	-	4.500
E1	6.200	-	6.600
e	0.650 BSC		
L	0.450	-	0.750
H	0.250 TYP		
θ	0°	-	8°
ccc	0.100		

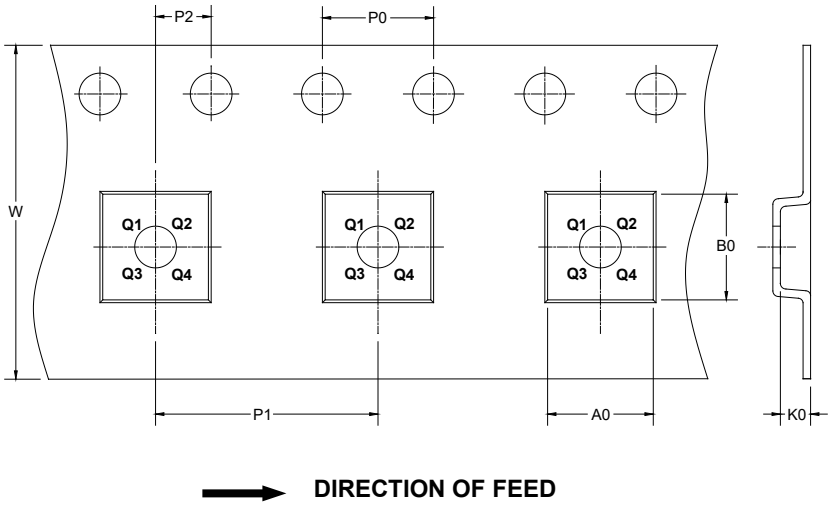
NOTES:
1. Body dimensions do not include mode flash or protrusion.
2. This drawing is subject to change without notice.
3. Reference JEDEC MO-153.

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



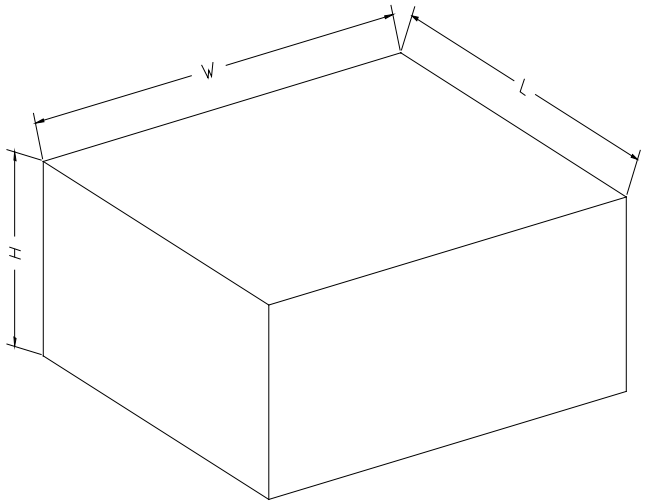
NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TSSOP-20	13″	16.4	6.80	6.90	1.50	4.0	8.0	2.0	16.0	Q1

DD0001

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
13″	386	280	370	5

DD0002