

12-Bit, 8-Channel, Digital System Monitor with Temperature Sensor, Internal/External Reference, and I²C Interface

GENERAL DESCRIPTION

The SGM90810 is an 8-channel system monitor especially designed for monitoring complicated system status. The power supply voltage range is from 3V to 5.5V. By configuring the internal registers, the signal input mode can be single-ended or pseudo-differential. The SGM90810 is equipped with a temperature sensor inside, built-in digital comparators and an interrupt output pin, which provides flexibility to monitor each channel and generate alarm signals when the input exceeds the thresholds. The SGM90810 integrates an internal reference, which makes this device a complete single chip solution for system monitor.

The SGM90810 system monitor has a true 12-bit sigma-delta ADC internally with I²C interfaces. The interface supports the Standard Mode (100kbps) and the Fast Mode (400kbps). The analog filters on the I²C bus provide excellent noise resistance performance. The SDA and SCL of I²C have a timeout reset function to prevent I²C bus locking. The SGM90810 provides a flexible sequencer for controlling channel conversions. The data conversion results of each channel is stored in independent registers. And each channel can be independently shut down to save power.

The SGM90810 is available in a Green TSSOP-16 package. It operates over the operating temperature range of -40°C to +125°C.

FEATURES

• True 12-Bit Sigma-Delta ADC Core

Power Supply Range: 3V to 5.5V

Total Unadjusted Error: ±0.16%

±1 LSB INL and ±1 LSB DNL

Operating Current: 0.25mA

Deep Shutdown Current: 15μA

Temperature Resolution: 0.5°C/LSB

• Temperature Accuracy:

-40°C to +125°C: ±2°C (MAX)

- Configurable Single-Ended and/or Pseudo-Diff. Inputs
- Selectable Internal 2.56V V_{REF} or External V_{REF}
- Digital Comparators for Each Channel
- Interrupt Pin nINT for Generating Alarm Signal
- I²C Serial Bus Interface Compatibility with 9 Selectable Addresses
- Time-Out Reset Feature for I²C interface
- Separate Channel Shutdown and Whole Chip **Deep Shutdown Mode to Minimize Power** Consumption
- Available in a Green TSSOP-16 Package

APPLICATIONS

Telecommunications Server Systems Industrial and Medical Systems Instrumentation and Test Equipment Voltage and Current Monitoring

TYPICAL APPLICATION

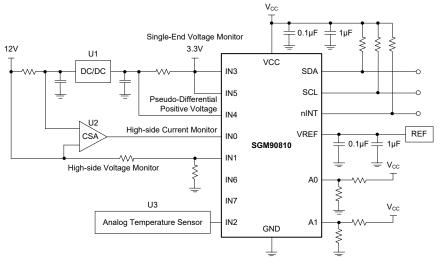


Figure 1. Typical Application Circuit



12-Bit, 8-Channel, Digital System Monitor with Temperature Sensor, Internal/External Reference, and I²C Interface

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION	
SGM90810	TSSOP-16	-40°C to +125°C	SGM90810XTS16G/TR	SGM17J XTS16 XXXXX	Tape and Reel, 4000	

MARKING INFORMATION

NOTE: XXXXX = Date Code, Trace Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, V _{CC}	6V
Voltage on SCL, SDA, A0, A1, nINT	0.3V to 6V
Voltage on IN0 to IN7, V _{REF}	0.3V to V _{CC} + 0.3V
Input Current at Any Pin	±5mA
Package Input Current	±30mA
Package Thermal Resistance	
TSSOP-16, θ _{JA}	99.3°C/W
TSSOP-16, θ _{JB}	69.1°C/W
TSSOP-16, θ _{JC}	38.7°C/W
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (Soldering, 10s)	+260°C
ESD Susceptibility (1)(2)	
HBM	±6000V
CDM	±2000V

NOTES:

- 1. For human body model (HBM), all pins comply with ANSI/ESDA/JEDEC JS-001 specifications.
- 2. For charged device model (CDM), all pins comply with ANSI/ESDA/JEDEC JS-002 specifications.

RECOMMENDED OPERATING CONDITIONS

Supply Voltage, V _{CC}	3V to 5.5V
Voltage on SCL, SDA, A0, A1, nINT	0.05V to 5.5V
Voltage on IN0 to IN7, V _{REF}	$-0.05V$ to $V_{CC} + 0.05V$
Operating Ambient Temperature Rang	e40°C to +125°C
Operating Junction Temperature Rang	e40°C to +125°C

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

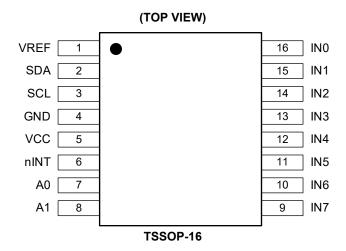
ESD SENSITIVITY CAUTION

This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATION

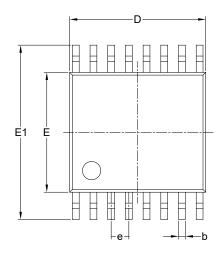


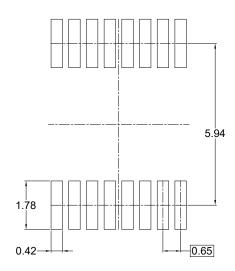
PIN DESCRIPTION

PIN	NAME	TYPE	FUNCTION			
1	VREF	AI	External Reference Input. The external reference voltage ranges from 1.25V to V_{CC} . Bypass this pin with a ceramic 1 μ F parallel with 0.1 μ F capacitor is strongly recommended. Leave it disconnected if using internal 2.56V reference. Note that the SGM90810 enables internal reference by default.			
2	SDA	DI/DO	Serial Bus Data Line, Open-Drain Input/Output.			
3	SCL	DI	Serial Bus Clock Line, Open-Drain Input.			
4	GND	G	Ground.			
5	VCC	Р	Power Supply Pin. Bypass this pin with a ceramic 1µF parallel with 0.1µF capacitor is strongly recommended.			
6	nINT	DO	Active-Low Open-Drain Interrupt Alarm Pin. A pull-up resistor is needed to make it work properly.			
7	A0	Tri-Level	I ² C Bus Device Address Select Pins. Support 9 addresses.			
8	A1	Inputs	1 O Bus Devide / Idai ess delett illis. Oupport o addicesses.			
9	IN7					
10	IN6					
11	IN5					
12	IN4	Al	Eight input pins which can be configured to different input modes. The input mode can be			
13	IN3	AI	configured as single ended mode or pseudo-differential mode through registers.			
14	IN2					
15	IN1					
16	IN0					

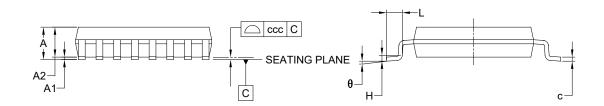
NOTE: Al: Analog Input, DI: Digital Input, DO: Digital Output, G: Ground, P: Power.

PACKAGE OUTLINE DIMENSIONS TSSOP-16





RECOMMENDED LAND PATTERN (Unit: mm)

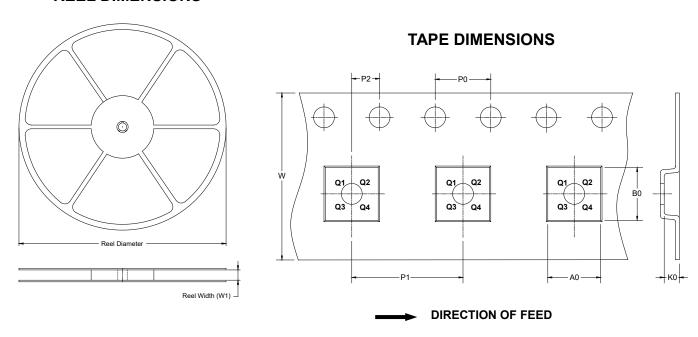


Symbol	Dir	nensions In Millimet	ters				
Symbol	MIN	NOM	MAX				
А	-	-	1.200				
A1	0.050	-	0.150				
A2	0.800	-	1.050				
b	0.190	-	0.300				
С	0.090	-	0.200				
D	4.860	-	5.100				
Е	4.300	-	4.500				
E1	6.200	-	6.600				
е	0.650 BSC						
L	0.450	0.750					
Н	0.250 TYP						
θ	0°	-	8°				
ccc	0.100						

- This drawing is subject to change without notice.
 The dimensions do not include mold flashes, protrusions or gate burrs.
- 3. Reference JEDEC MO-153.

TAPE AND REEL INFORMATION

REEL DIMENSIONS

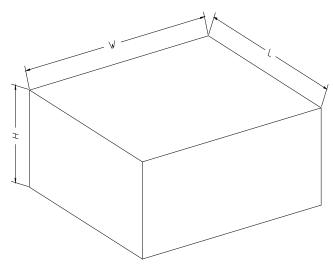


NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
TSSOP-16	13"	12.4	6.80	5.40	1.50	4.0	8.0	2.0	12.0	Q1

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton	
13″	386	280	370	5	DD0002